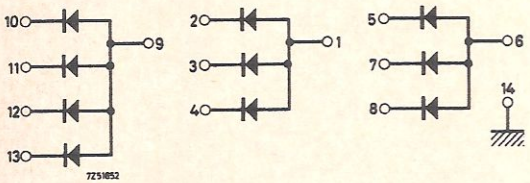
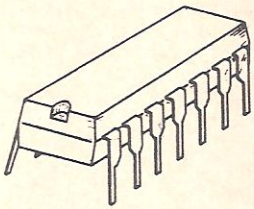
The FC family of DTL silicon monolithic integrated circuits is designed for medium speed digital equipment in computing, telecommunication, instrumentation and industrial control.

# TRIPLE GATE INPUT EXPANDER
# (10-DIODE ARRAY)



The FCY101, comprises three independent diode arrays. It is intended primarily for expanding the fan-in capability of FCH gates provided with an expansion input terminal.

Red binder Integrated circuits

**CHARACTERISTICS** (for each diode)

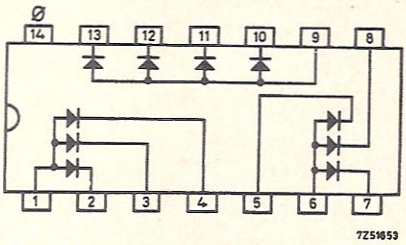
| | | | $T_{amb}$ (°C) 25 | 75 | |
|---|---|---|---|---|---|
| Reverse breakdown voltage at $I_R$ = 50 μA | $V_{(BR)R}$ | min. | 8.0 | | V |
| Reverse leakage current at $V_R$ = 8.0 V | $I_R$ | max. | 1.0 | 25 | μA |
| Forward voltage at $I_F$ = 2.0 mA | $V_F$ | max. | 1.0 | | V |
| Capacitance at $V_R$ = 0; f = 1 MHz | $C_d$ | max. | 11 | | pF |
| Reverse recovery time at $I_F = I_R$ = 2.0 mA | $t_{rr}$ | typ. | 4 | | ns |
| | | max. | 11 | | ns |

**RATINGS** (Limiting values)

| | | | | |
|---|---|---|---|---|
| Reverse voltage | $V_R$ | max. | 8.0 | V |
| Forward current | $I_F$ | max. | 30 | mA |
| Storage temperature | $T_{stg}$ | | −35 to +125 | °C |
| Operating ambient temperature | $T_{amb}$ | | 0 to +75 | °C |

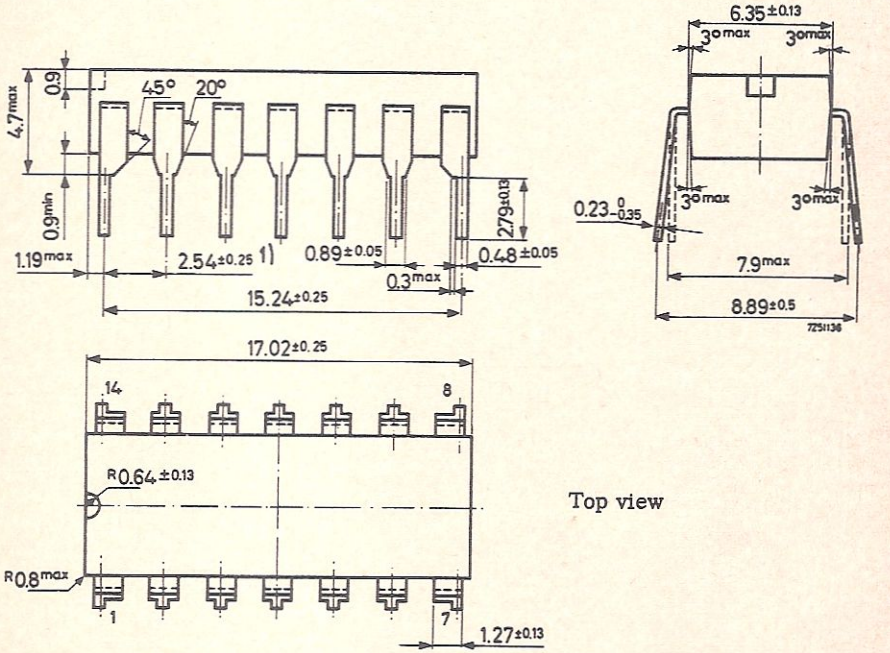
**PACKAGE:** D1 (Dual in-line)

## TERMINAL CONNECTIONS (Top view)




## PACKAGE OUTLINE

Dimensions in mm




Top view

1) Tolerance non-cumulative